501.39868X00



EXPEDITED PROCEDURE PURSUANT

TO 37 CFR §1.116 REQUESTED

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

T. SAITO et al

Serial No.:

09/850,162

Filed:

May 8, 2001

For:

A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND A

METHOD OF MANUFACTURING THE SAME

Group:

2812

Examiner:

R. E. POMPEY

CONF. No.

9657

AMENDMENT AFTER FINAL

MS: AFTER FINAL

Commissioner For Patents

POB 1450

Alexandria, VA 22313-1450

Sept. 29, 2003 (Monday)

Sir:

In response to the Final Office Action, dated May 29, 2003, the period of response for which extension of time is requested in the attached Petition for J belo. Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Claims.

Remarks are included following the Amendment.

Ando N/E Mundler 10/31/03